As an inventor named below or on any attached continuation page, I hereby declare that:

My residence, post office address and citizenship are as stated next to my name.

I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled METHOD OF FABRICATING CHIP-SCALE PACKAGES AND RESULTING STRUCTURES, the specification of which (check one):

is attached hereto.	tates application serial no	nd was amended on			
	rnational application no an	d was amended under PCT Article 19 on	·		
referred to above.	erstand the coments of the above-identif	ed specification, including the claims, as ame	nided by any ar	nenumem	
I acknowledge the duty to disclose to the U matter claimed in this application, as "material	I.S. Patent and Trademark Office all info	rmation known to me to be material to the paral Regulations § 1.56.	tentability of th	e subject	
I hereby claim foreign priority benefits und	der Title 35 United States Code 8 1190	a)-(d) or § 365(b) of any foreign application(s)	) for patent or i	inventor's	
certificate or § 365(a) of any PCT international	Il application(s) designating at least one of	country other than the United States of Americ	ca listed below	and on any	
attached continuation page and have also ident any PCT international application(s) designation on which priority is claimed.	ified below and on any attached continung at least one country other than the Un	ition page any foreign application for patent of ited States of America having a filing date bef	or inventor's ce fore that of the	application(s)	
Prior foreign/PCT application(s):		Priority		y Claimed	
(number)	(country)	(day/month/year filed)	Yes	No	
T				<del></del>	
(number)  Thereby claim the benefit under Title 35.	(country)	(day/month/year filed)	Yes	No	
Regulations § 1.56 which became available be (application serial no.)	(filing date)	(status - pending, patented o		- application.	
(application serial no.)	(filing date)	(status - pending, patented of	or abandoned)		
≅ (application serial no.)	(ming date)	(satis periong, paremet	n dodinoonod)		
I hereby claim the benefit under Title 35,  (provisional application no.)	United States Code, § 119(e) of any Uni	ted States provisional application(s) listed belo	ow:		
(provisional application no.)	(filing date)	_			
I hereby appoint the following Registered therewith:	Practitioners to prosecute this application	and to transact all business in the Patent and	Trademark Off	fice connected	
David V. Trask, Reg. No. 22,012	William S. Britt, Reg. No. 20,969 Joseph A. Walkowski, Reg. No. 28	Thomas J. Rossa, Reg. No. 26, 765 James R. Duzan, Reg. No. 28,			
Laurence B. Bond, Reg. No. 30,549 Allen C. Turner, Reg. No. 33,041	Kent S. Burningham, Reg. No. 30,4	Edgar R. Cataxinos, Reg. No.	39,931		
Stephen R. Christian, Reg. No. 32,687 Paul C. Oestreich, Reg. No. 44,983	Brick G. Power, Reg. No. 38,581 Devin R. Jensen, Reg. No. 44,805	Kenneth B. Ludwig, Reg. No.: Eleanor V. Goodall, Reg. No.:			
Kenneth C. Booth, Reg. No. 42,342 Michael L. Lynch, Reg. No. 30,871	Samuel E. Webb, Reg. No. 44,394 Lia M. Pappas, Reg. No. 34,095	David L. Stott, Reg. No. 43,93			
TRA P.O	eph A. Walkowski, telephone no. (801) S ASK, BRITT & ROSSA I. BOX 2550 I. Lake City, Utah 84110	32-1922.			
	erein of my own knowledge are true and tith the knowledge that willful false state	I that all statements made on information and ments and the like so made are punishable by alse statements may jeopardize the validity of	fine or impriso	onment, or	
Full name of first joint inventor: Tongbi Jian Inventor's signature	sout Je	Date			
Residence: Boise, Idaho Citizenship: P.R. China Post Office Address: 197 E. Mallard Drive,					

Invention title: METHOD OF FABRICATING CHIP-SCALE PACKAGES AND RESULTING STRUCTURES

Inventor name(s) appearing on first declaration page: Tongbi Jiang

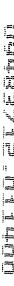
Additional original, first and joint inventor(s):

Full name of second joint inventor: Edw					
inventor's signature	nd A.	SMEX	Date	1-11-00	
North Control of the					

Residence: Boise, Idaho

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Post Office Address: 3188 E. Whitman, Boise, Idaho 83714







## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Serial No.: Jiang et al.

Examiner:

Unknown

Not Yet Assigned Group Art Unit: Attorney Docket No.: Unknown 3815US (98-0670)

Filed: Title:

METHOD OF FABRICATING CHIP-SCALE PACKAGES AND RESULTING STRUCTURES

## POWER OF ATTORNEY BY ASSIGNEE AND CERTIFICATE UNDER 37 CFR § 3.73(b)

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

MICRON TECHNOLOGY, INC., assignee of the entire right, title and interest by assignment from the inventor(s) in the above-identified application, hereby appoints the following attorneys and agents:

David V. Trask, Reg. No. 22,012 Laurence B. Bond, Reg. No. 30,549 Allen C. Turner, Reg. No. 33,041 Stephen R. Christian, Reg. No. 32,687 Paul C. Oestreich, Reg. No. 44,983 Kenneth C. Booth, Reg. No. 42,342 Michael L. Lynch, Reg. No. 30,871 William S. Britt, Reg. No. 20,969 Joseph A. Walkowski, Reg. No. 28,765 Kent S. Burningham, Reg. No. 30,453 Brick G. Power, Reg. No. 38,581 Devin R. Jensen, Reg. No. 44,805 Samuel E. Webb, Reg. No. 44,394 Lia M. Pappas, Reg. No. 34,095 Thomas J. Rossa, Reg. No. 26,799 James R. Duzan, Reg. No. 28,393 Edgar R. Cataxinos, Reg. No. 39,931 Kenneth B. Ludwig, Reg. No. 42,814 David L. Stott, Reg. No. 43,937 Eleanor V. Goodall, Reg. No. 35,162

as its attorneys with full power of substitution to prosecute this application and all applications claiming filing date priority therefrom and to transact all business in the U.S. Patent and Trademark Office in connection therewith.

The above-identified assignee hereby elects, pursuant to 37 C.F.R. § 3.71, to conduct the proserution of the above-identified patent application to the exclusion of the inventor(s).

A chain of title from the inventor(s) of the above-identified patent application to the above-identified assignee is shown:

- [] In an assignment recorded in the U.S. Patent and Trademark Office at Reel, Frame.
- [X] In an assignment filed herewith for recordation, a true copy of which is attached hereto.

The undersigned has reviewed the above-identified assignment and, to the best of his knowledge and belief, title is in the above-identified assignee.

The undersigned further avers that he is empowered to make and sign the foregoing certification on behalf of the above-identified assignee, and to take the action set forth herein on its behalf.

Please direct all communications regarding the above-identified application to:

Joseph A. Walkowski, TRASK, BRITT & ROSSA P.O. Box 2550 Salt Lake City, UT 84110 Tele: (801) 532-1922 Fax: (801) 531-9168

Respectfully Submitted,

MICRON TECHNOLOGY, INC.

Date: 12, 2000

Michael L. Lynch, Esq. Reg. No. 30,871 Chief Patent Counsel,

MICRON TECHNOLOGY, INC.

N:\2269\3815\POA by Assignee.wpd3